

FIG. 1

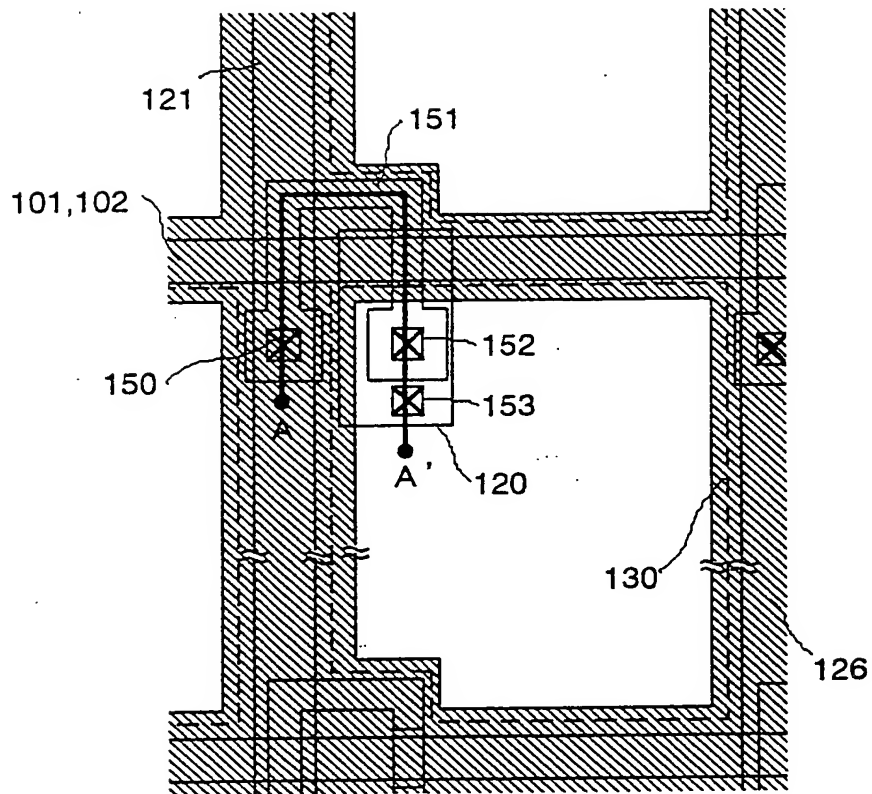


FIG. 2A

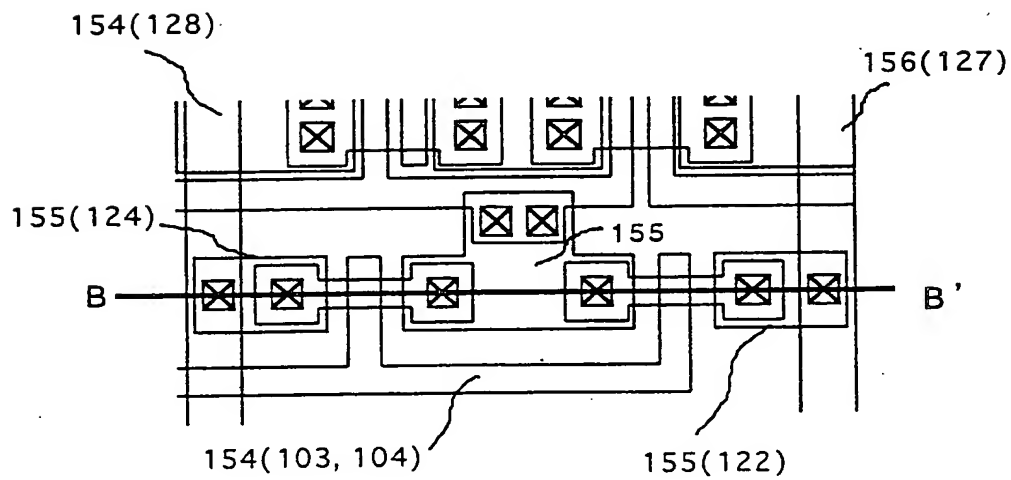
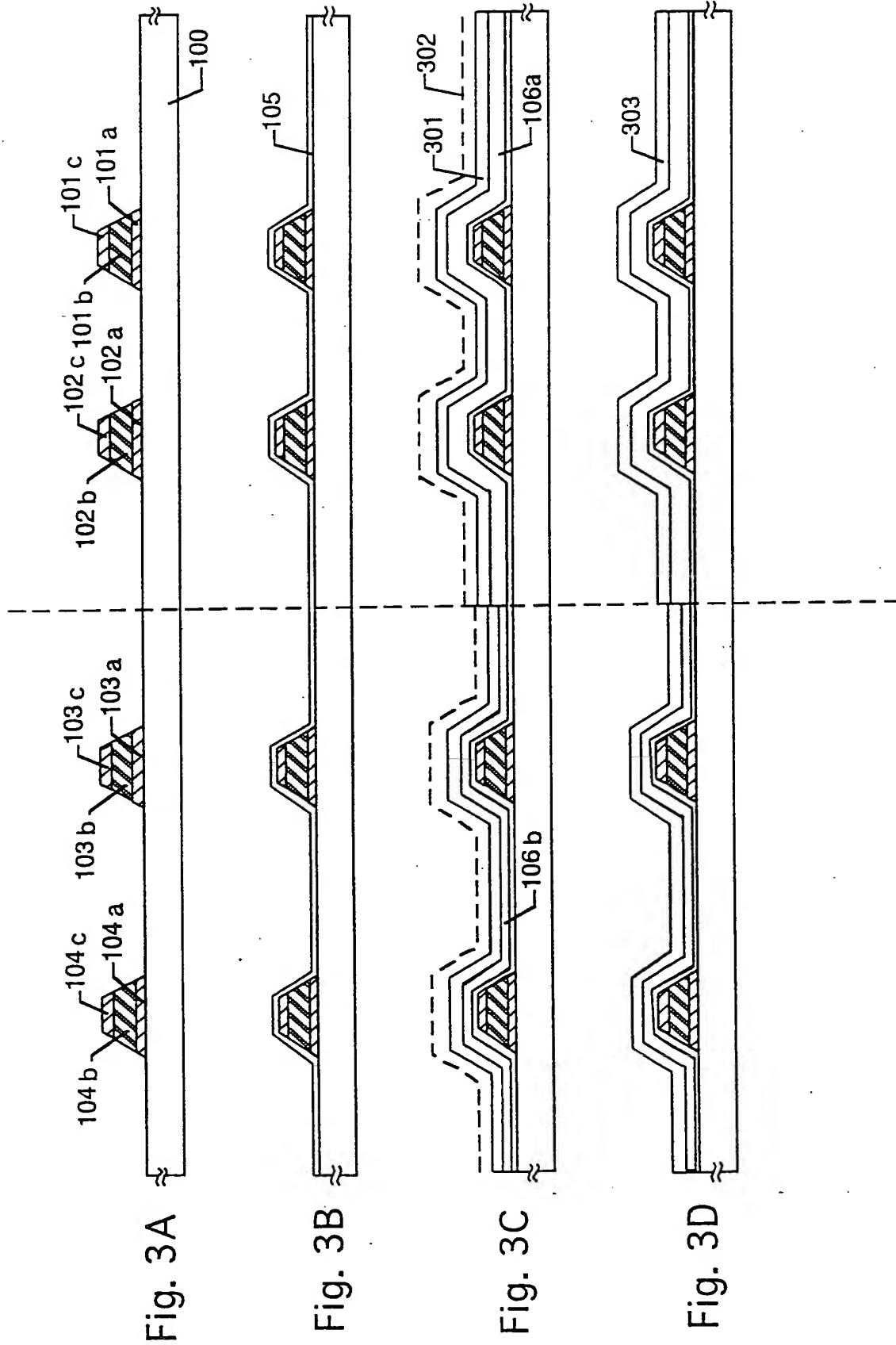


FIG. 2B



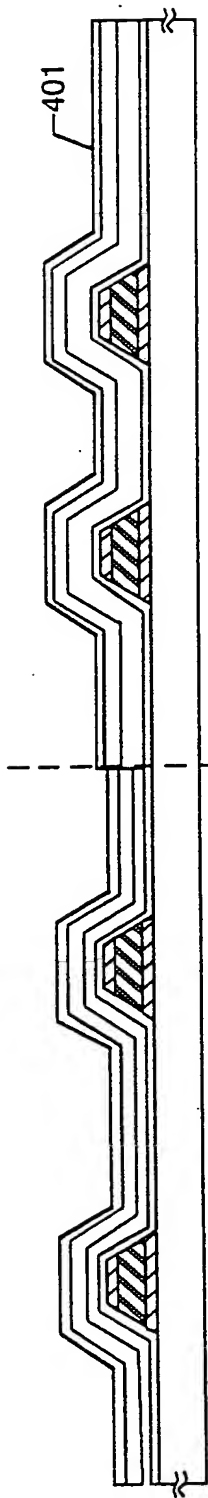


Fig. 4A

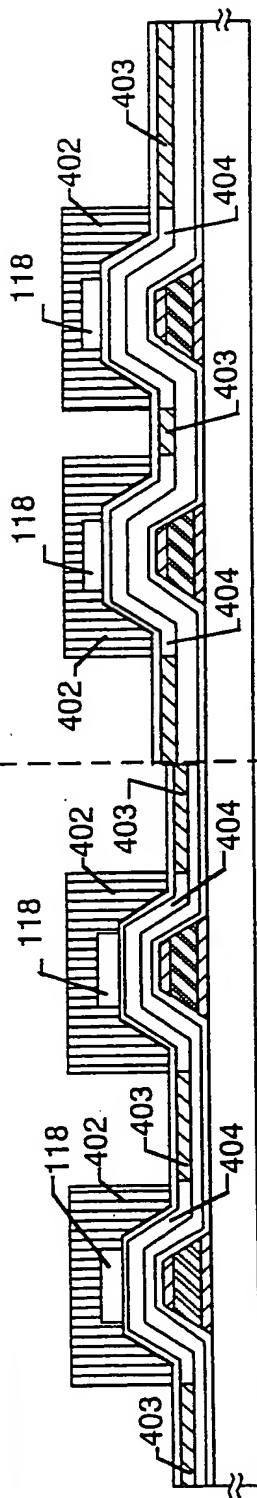


Fig. 4B

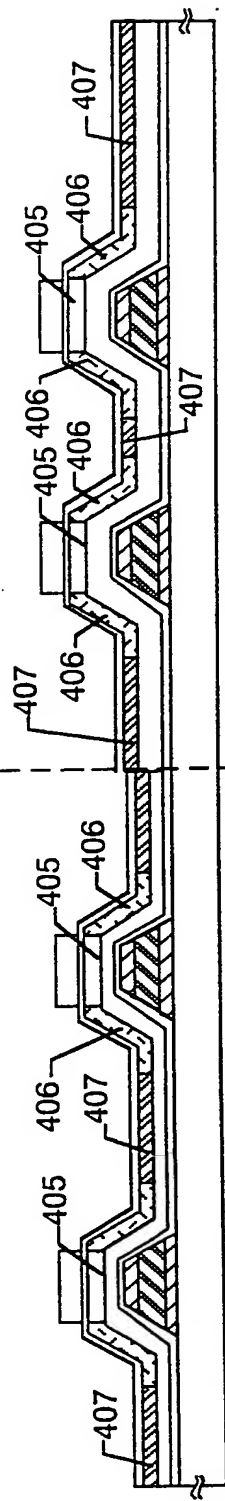


Fig. 4C

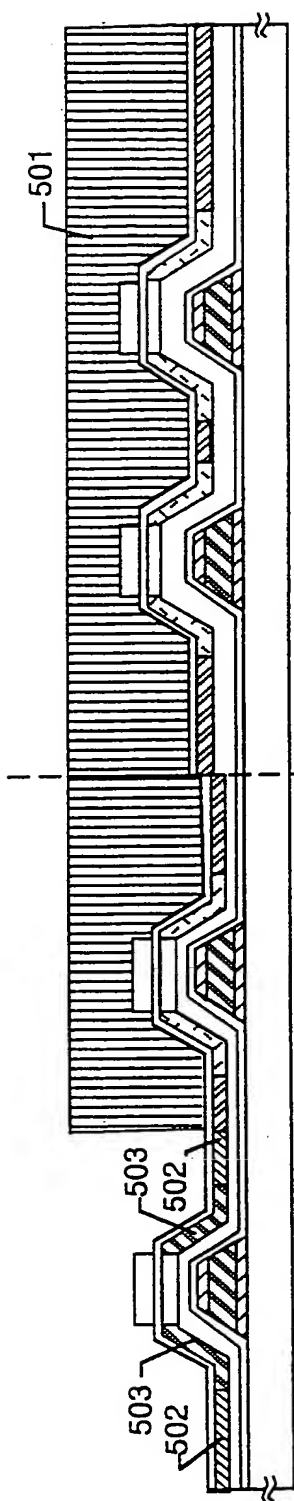


Fig. 5A

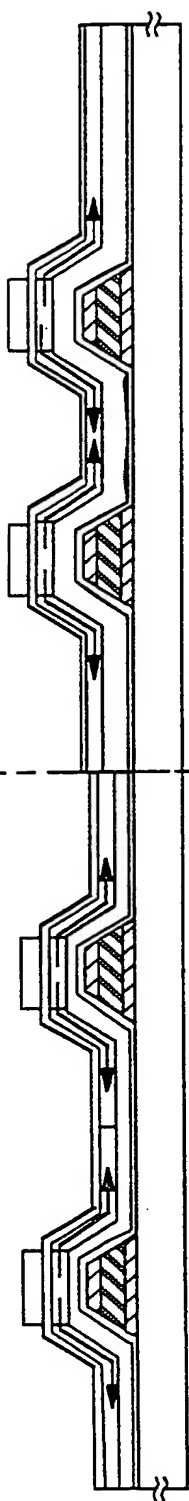


Fig. 5B

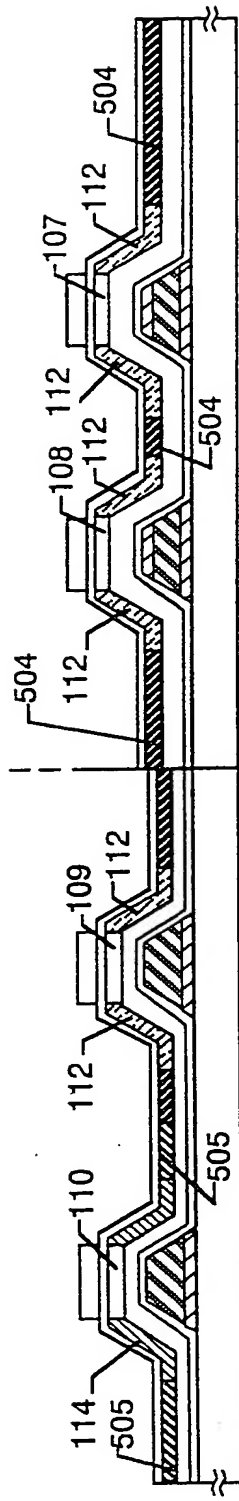


Fig. 5C

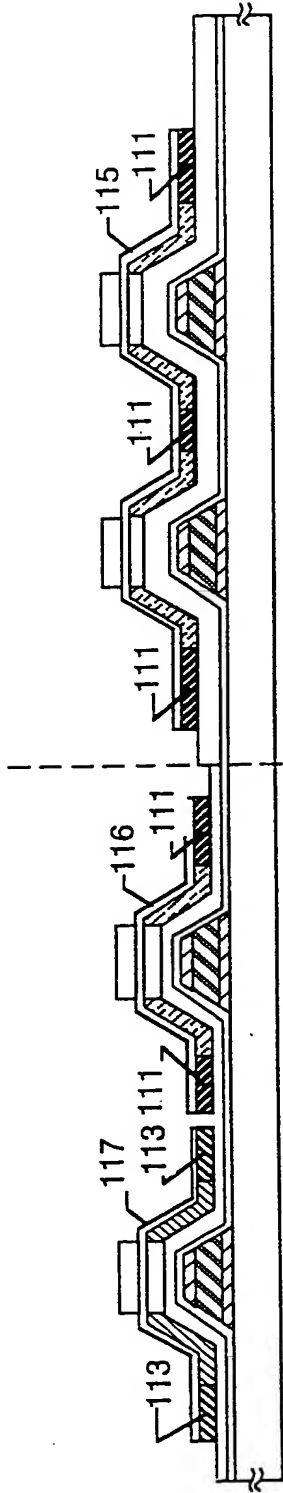


Fig. 6A

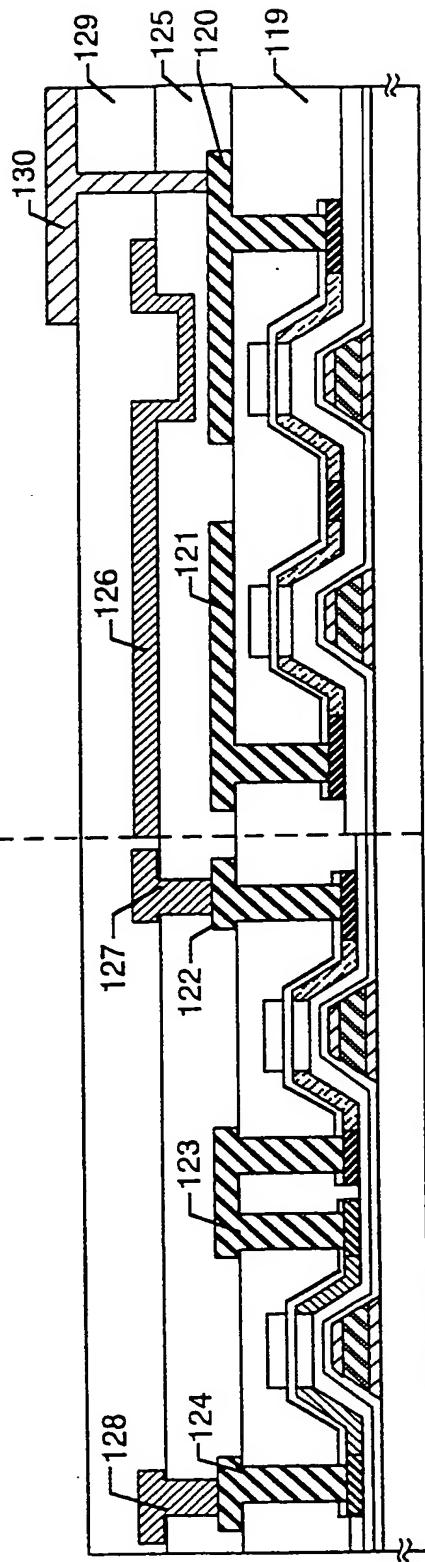
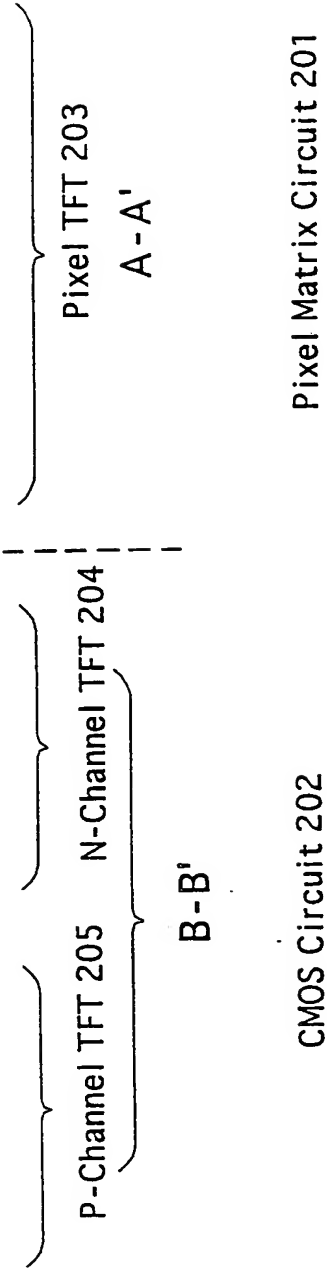


Fig. 6B



CMOS Circuit 202

Pixel Matrix Circuit 201

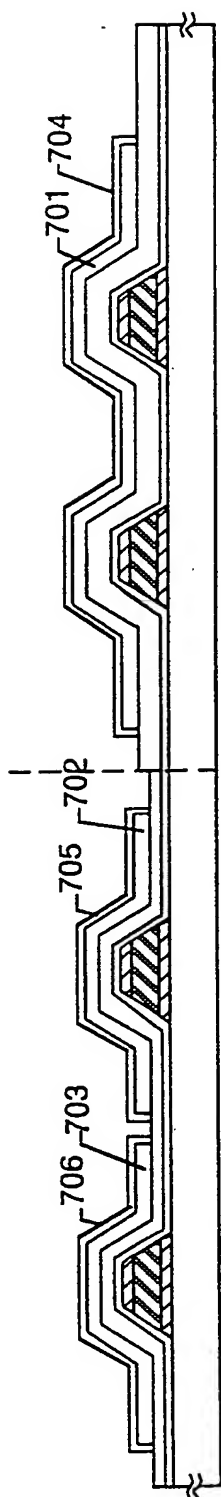


Fig. 7A

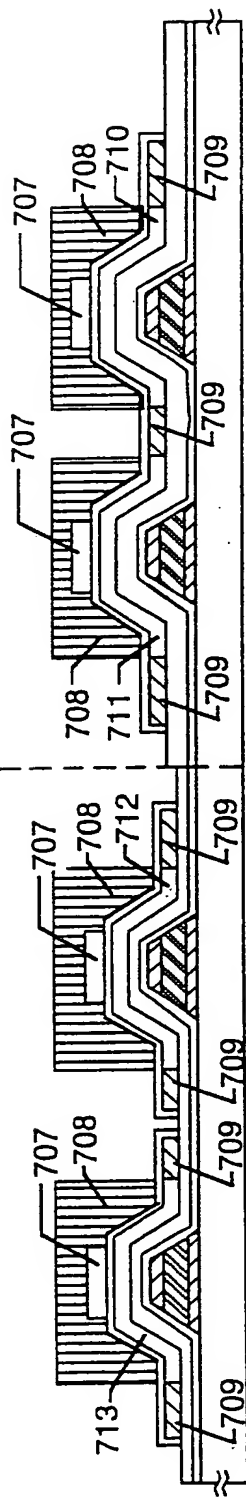


Fig. 7B

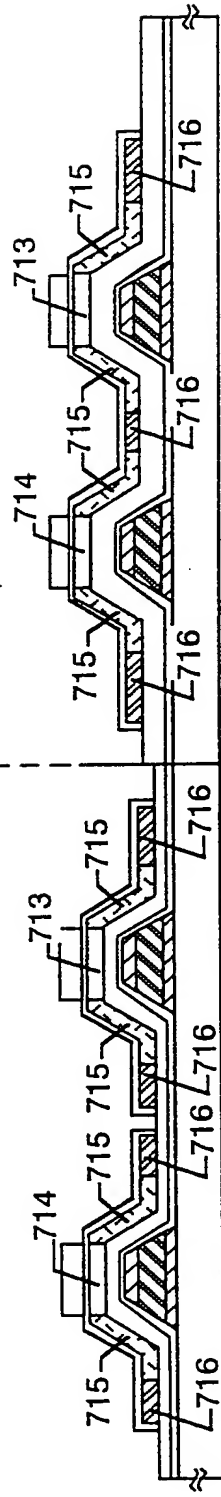


Fig. 7C

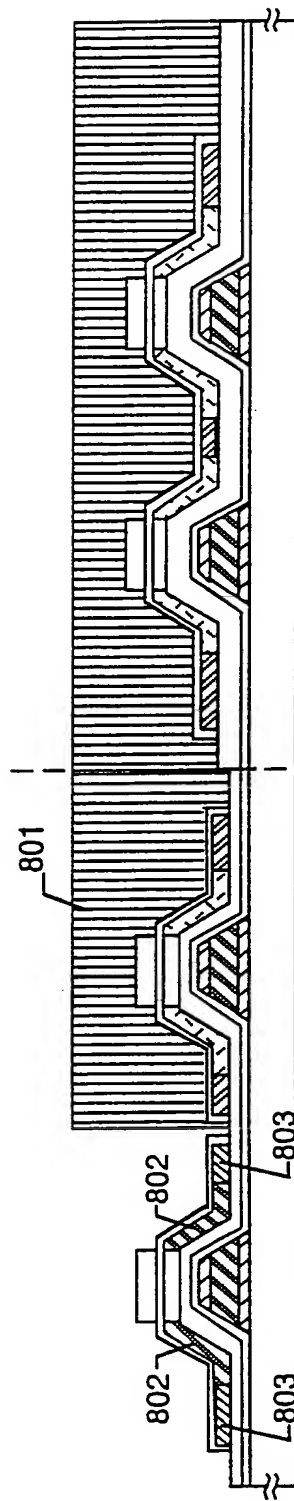


Fig. 8A

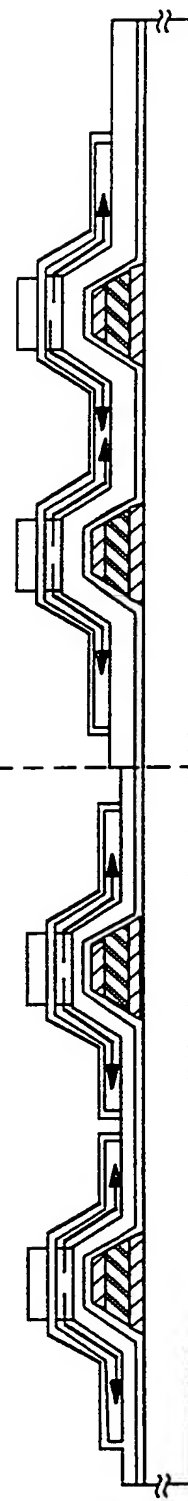


Fig. 8B

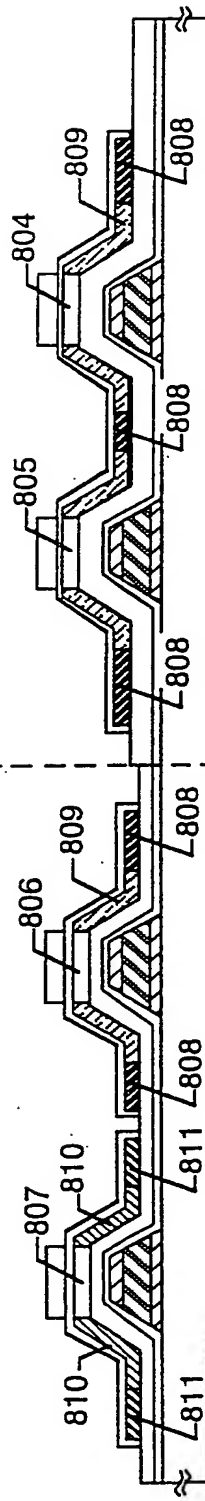


Fig. 8C



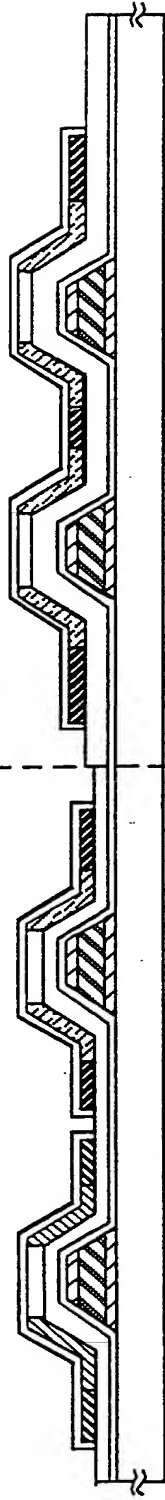


Fig. 9A

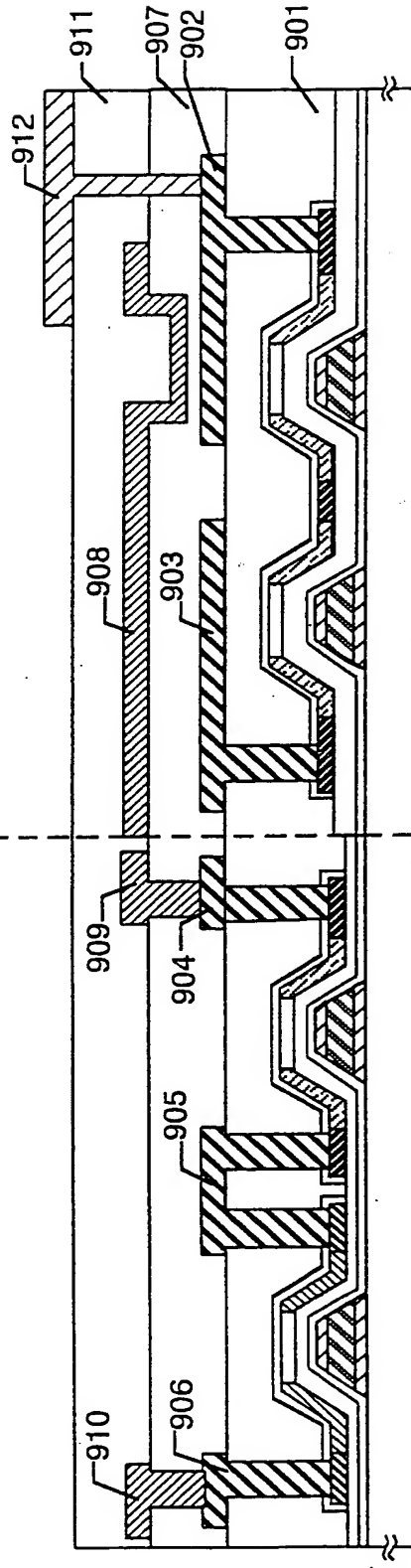


Fig. 9B

P-Channel TFT 205    N-Channel TFT 204

B-B'

Pixel TFT 203

A-A'

CMOS Circuit 202

Pixel Matrix Circuit 201

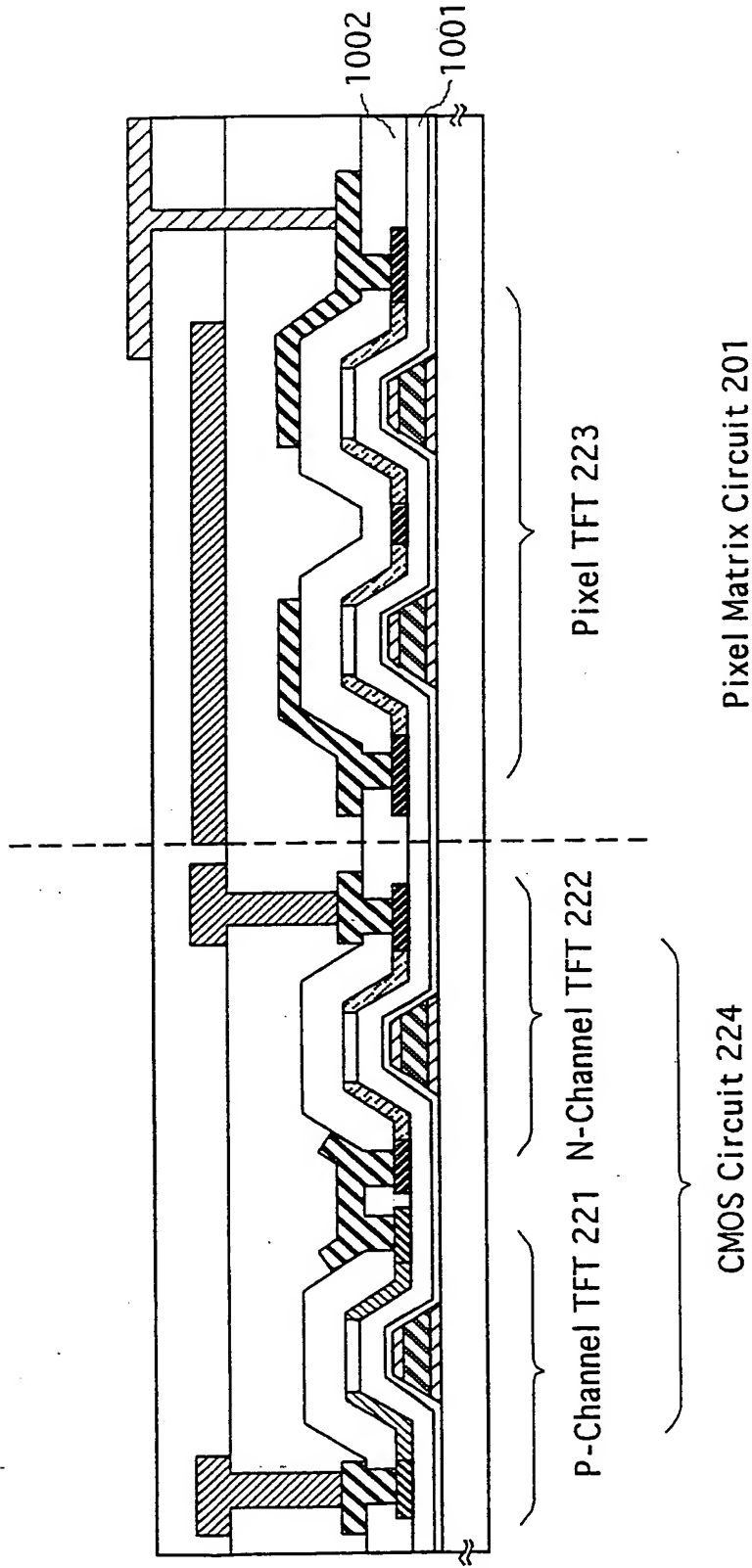


Fig. 10

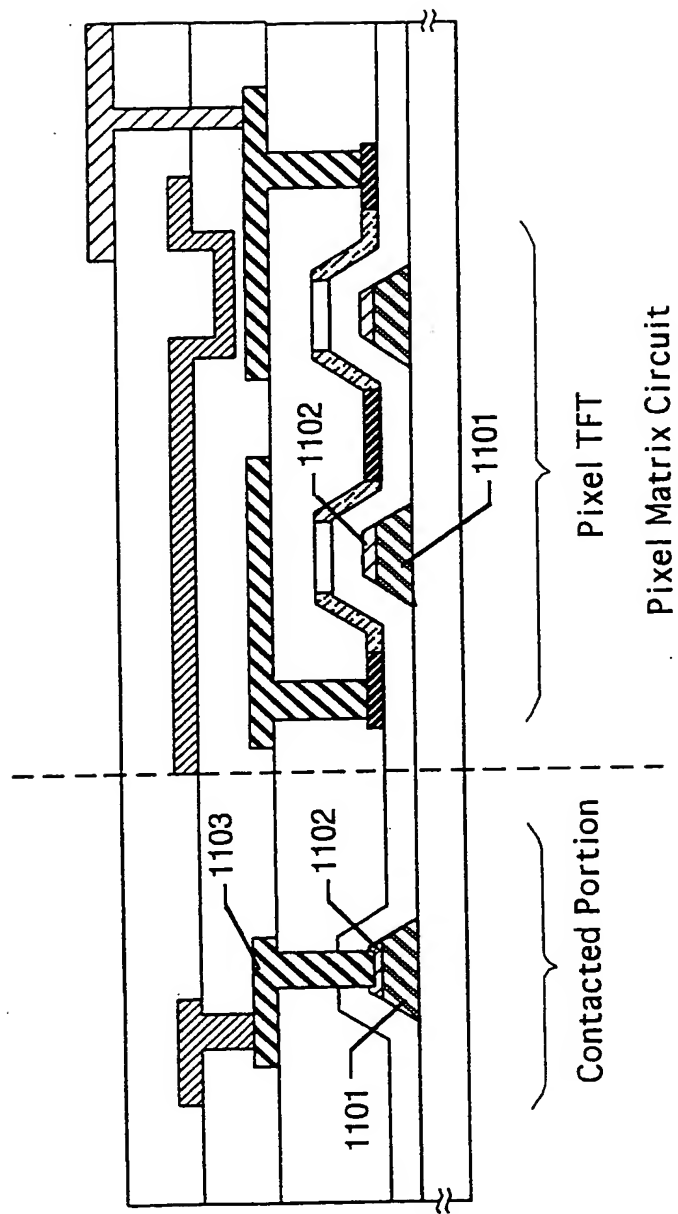


Fig. 11

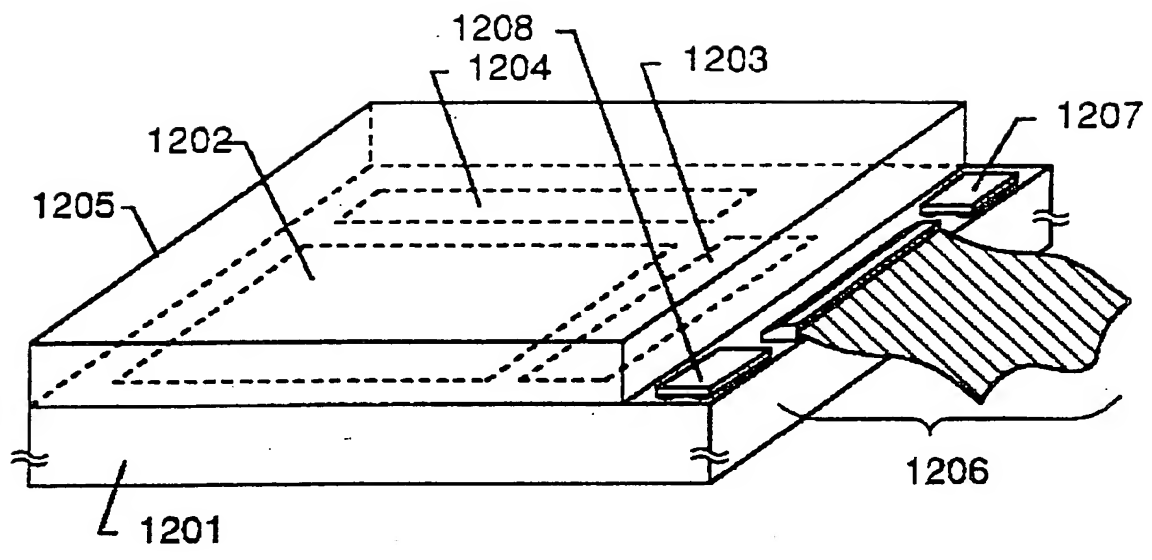


Fig. 12A

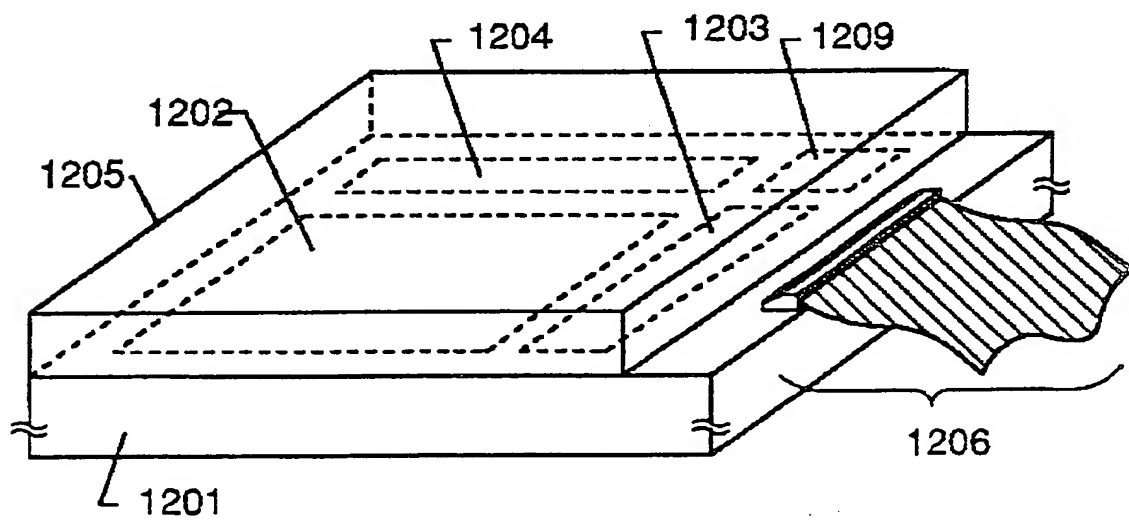


Fig. 12B

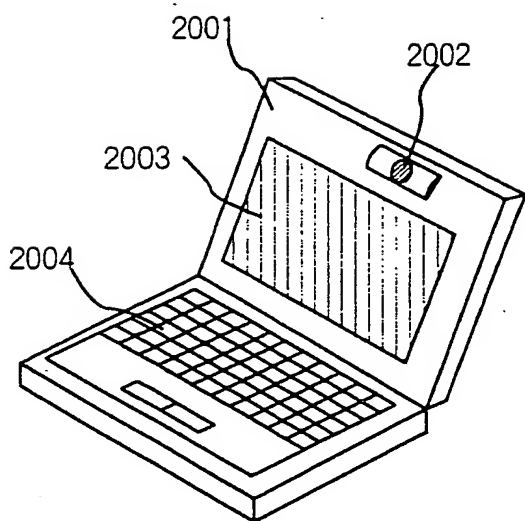


Fig. 13A

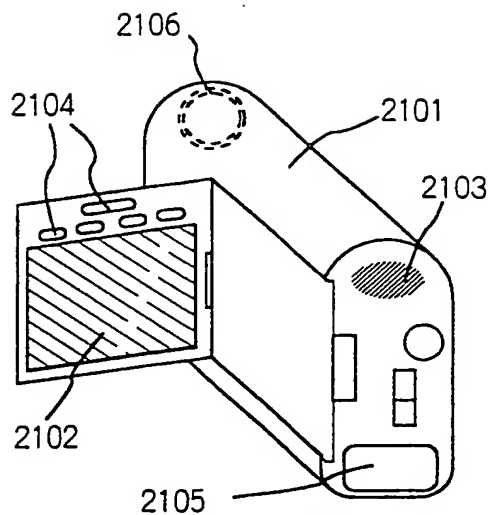


Fig. 13B

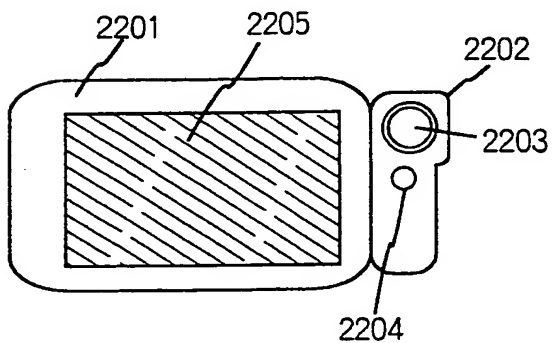


Fig. 13C

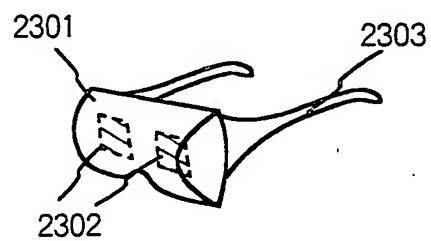


Fig. 13D

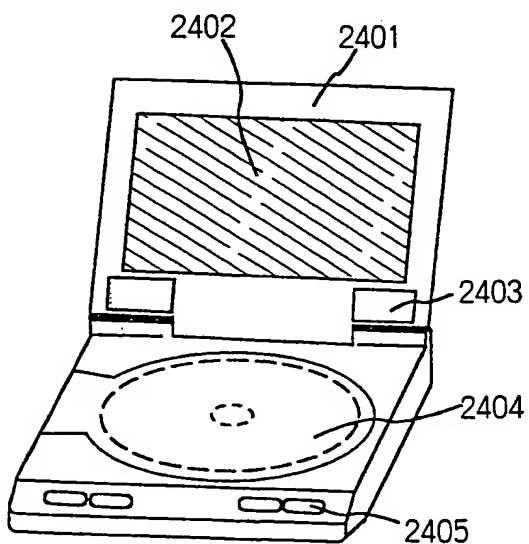


Fig. 13E

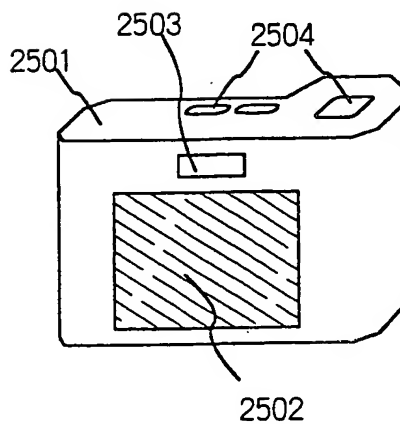


Fig. 13F

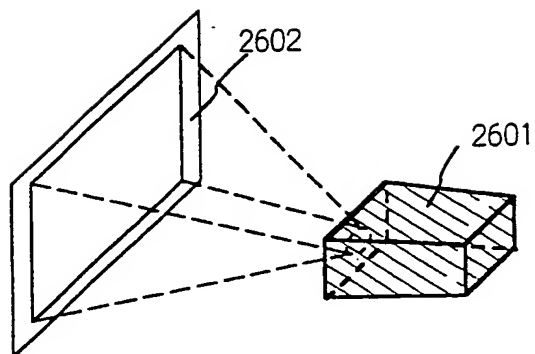


Fig. 14A

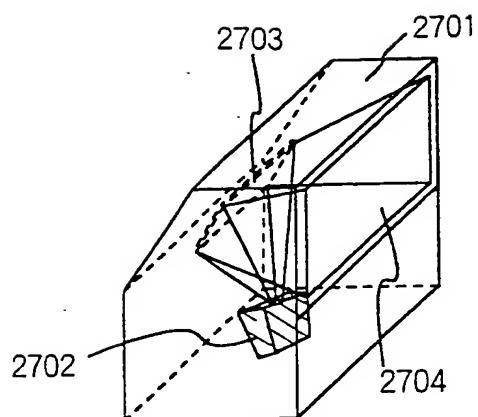


Fig. 14B

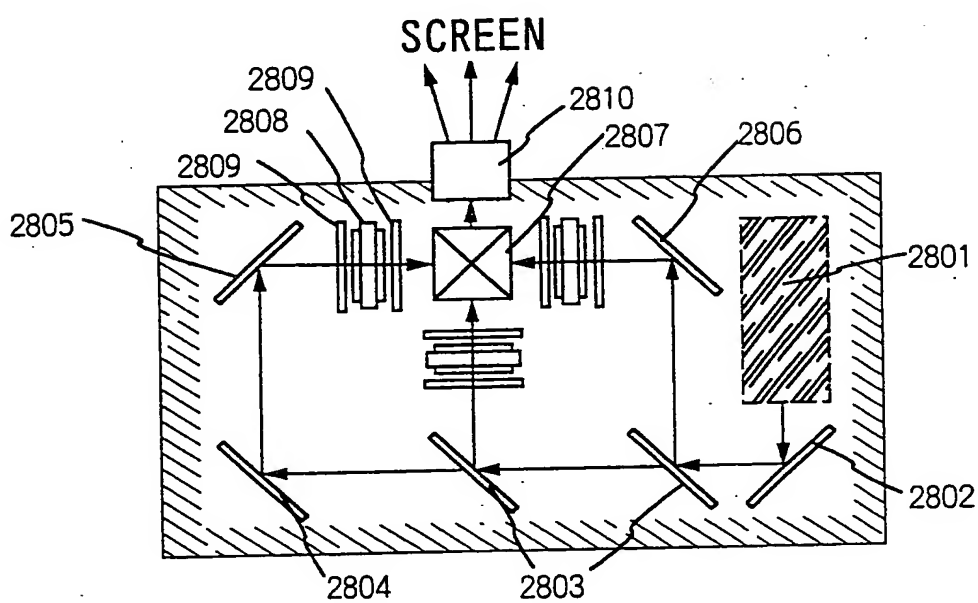


Fig. 14C

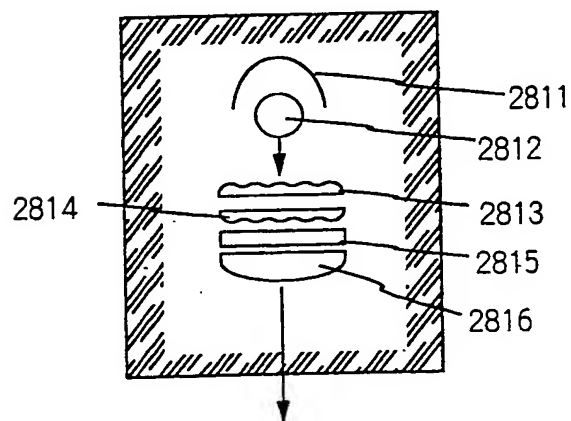


Fig. 14D